

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	47592	select\$4 with (multiplex\$4 mux\$4 concatenat\$4) with output\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 23:36
L2	31	1 with (rate bandwidth) with match\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 23:50
L3	1820	370/536-540.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 23:49
L4	2386	370/465.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 23:50
L5	4144	3 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 23:50
L6	478	5 and 1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 23:50
L7	43190	(rate bandwidth) with match\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 23:50
L8	51	7 and 6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 23:51
L9	47	8 and (clock frequenc\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 23:51

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4849	(mux demux multiplex\$4 demultiplex\$4 assembl\$4 disassemb\$4) with (flip adj flop) with (clock tim\$4 frequenc\$4 phase)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 17:20
L2	1315	1 with select\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 16:06
L3	96	2 with (seri\$4 sequence concatena\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 16:25
L4	46	3 and (packet cell frame)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 16:23
L5	87	"5,084,874"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 16:23
L6	107	2 with (seri\$4 sequen\$4 concatena\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 16:25
L7	49487	match\$4 with (rate bandwidth resource)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 16:27
L8	1	6 and 7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 16:26
L9	10	6 same (rate bandwidth resource)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 16:27

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L10	412	(mux demux multiplex\$4 demultiplex\$4 assembl\$4 disassemb\$4) with (flip adj flop) with (clock tim\$4 frequenc\$4 phase) with cycle	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 16:42
L11	42	10 with (seri\$4 sequen\$4 concaten\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 16:46
L12	13	10 with (rate bandwidth)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 17:18
L13	28	10 same (rate bandwidth)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 16:45
L14	231	10 and (rate bandwidth)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 16:46
L15	89	10 same (seri\$4 sequen\$4 concaten\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 16:46
L16	55	15 and (rate bandwidth)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 16:52
L17	1600	match\$4 with (rate bandwidth) with (memory peripheral)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 16:55
L18	4	17. and 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 16:53
L19	929	match\$4 with ((data adj path adj width) (clock adj (rate speed)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 16:58

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L20	4	19 and 1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 16:56
L21	23	match\$4 with (data adj path adj width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 17:19
L22	22	match\$4 with data with path with width with memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 17:09
L23	218	match\$4 with (clock adj cycle) with memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 17:09
L24	3	23 and 1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 17:15
L25	420	370/472	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 17:15
L26	3645	370/474	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 17:16
L27	1126	370/476	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 17:16
L28	4359	25 26 27	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 17:16
L29	6	28 and 1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 17:18

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L30	18	NIXON adj WALTER.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 17:19
L31	34	LEE adj WHAY adj SING.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 17:19
L32	32	CHONG adj FAY.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 17:19
L33	69	30 31 32	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 17:20
L34	92	((mux demux multiplex\$4 demultiplex\$4 assembl\$4 disassemb\$4) with ((flip adj flop) cell register storage slot) with (clock tim\$4 frequenc\$4 phase) with (rate bandwidth) with (seri\$4 sequen\$5 concaten\$6)).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 17:23
L35	1	34 and 33	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 17:22
L36	376	((mux demux multiplex\$4 demultiplex\$4 assembl\$4 disassemb\$4) with (clock tim\$4 frequenc\$4 phase) with (rate bandwidth) with (seri\$4 sequen\$5 concaten\$6)).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 17:23
L37	1	36 and 33	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 17:23